## Amendments to the Claims:

This listing of claims will replace all prior version, and listings, of claims in the application:

## **Listing of Claims:**

1. (currently amended) A driving method for a Thin Film Transistor (TFT) array, capable of saving power, comprising the steps of:

implementing an Application Specific Integrated Circuit chip; determining a predetermined mode;

dividing a Thin Film Transistor array frame into a plurality of zones according to the predetermined mode, wherein the plurality of zones are grouped into graphic and non-graphic regions; and

signaling a control signal by the Application Specific Integrated Circuit to determine the driving type required for each zone according to the plurality of zones grouped.

- 2. (original) The method of Claim 1, wherein the predetermined mode is a standby mode.
- 3. (original) The method of Claim 1, wherein the predetermined mode is a graphic mode.
- 4. (original) The method of Claim 1, wherein the predetermined mode is a video mode.

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- 5. (original) The method of Claim 1, wherein the predetermined mode is dictated by the manufacturer.
- 6. (original) The method of Claim 1, wherein the graphic and non-graphic regions located on a frame are determined by the manufacturer.
- 7. (original) The method of Claim 1, wherein the driving type in the graphic region uses a line inversion.
- 8. (original) The method of Claim 1, wherein the driving type in the nongraphic region uses a frame inversion.
- 9. (original) The method of Claim 1, wherein the step of determining a predetermined mode is performed by a central processing unit (CPU).
- 10. (original) The method of Claim 1, wherein the step of determining a predetermined mode is performed by an operating system.
- 11. (currently amended) The method of Claim 1, further comprising a step of signaling the data associated with the plurality of zones to the ASIC chip after the dividing step.
- 12. (new) A driving method for a Thin Film Transistor array, capable of saving power, comprising:

dividing a Thin Film Transistor array frame into a first zone and a second zone; and

driving the first and second zones respectively with a first driving type



and a second driving type, wherein the first and second driving types are different from each other.

- 13. (new) The method as claimed in Claim 12, further comprising: implementing an Application Specific Integrated Circuit chip; and choosing the first mode or the second mode.
- 14. (new) The method as claimed in Claim 12, further comprising:
  grouping the first and second zones grouped into a graphic and nongraphic regions respectively.
- 15. (new) The method as claimed in Claim 12, wherein the first driving type is a line inversion and the second driving type is a frame inversion.
- 16. (new) An LCD display, comprising:

an Application Specific Integrated Circuit chip determining the first and second driving types; and

- a Thin Film Transistor array, comprising:
  - a first zone driven with a first driving type; and
- a second zone driven with a second driving type different from the first driving type.
- 17. (new) The LCD display as claimed in Claim 16, wherein the first and second zones are respectively grouped into a graphic and non-graphic regions in the first mode.
- 18. (new) The LCD display as claimed in Claim 16, wherein the first driving

and

type is a line inversion and the second driving type is a frame inversion.